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(54) CIRCUIT FOR CONTROLLING REFRESH

resetting 2.

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(57) Abstract:

PURPOSE: To eliminate the futile use of electric power owing to refresh by permitting the generation cycle of a refresh control signal to agree with DRAM information holding time.

CONSTITUTION: A reset cycle switching circuit 4 switch-controlling the transmission of frequency dividing signals CD1-CD4 by non-melting or melting fuses F1-F8 so as to switch the cycle of a reset signal RST is provided between a counter 1 and a logical gate for

